

- 72
142. (New) A system for conserving battery life in a battery operated device comprising:  
a receiver for receiving a transmission that includes a postamble;  
a microprocessor coupled to the receiver, the microprocessor periodically partially  
awakening to determine whether the transmission is likely a FLP by examining the postamble.
143. (New) The system of claim 142, wherein the postamble is Manchester encoded.
144. (New) The system of claim 142, wherein the postamble includes a predetermined number  
of transitions.
145. (New) The system of claim 142, wherein the microprocessor determines whether the  
transmission is likely a FLP by determining if the transmission includes a predetermined number  
of transitions.
146. (New) The system of claim 142, wherein the microprocessor is configured to collect data  
from a sensor.
147. (New) The system of claim 142, wherein the battery operated device includes a  
transmitter.
148. (New) The system of claim 147, wherein the postamble shortens the amount of time that  
the transmitter is ON.
149. (New) The system of claim 147, wherein the postamble increases the amount of time  
between the FLP and a RLP sent from the battery operated device, allowing the battery operated  
device enough time to stabilize its transmitter on an appropriate return link channel.
150. (New) The system of claim 147, wherein the transmitter is turned ON only in response to  
a valid FLP.
151. (New) The system of claim 147, wherein the transmitter uses a successive approximation  
routine to obtain data from one device out of a plurality of devices.

152. (New) The system of claim 142, wherein the battery operated device only responds to a valid FLP having a specific identification number associated with the device.

153. (New) The system of claim 142, wherein the battery operated device includes a transmitter having a phase locked loop (PLL).

154. (New) The system of claim 153, wherein the postamble increases the amount of time that the battery operated device has to obtain a PLL lock on an appropriate return link channel.

155. (New) The system of claim 153, wherein the postamble shortens the amount of time that the PLL is ON.

156. (New) The system of claim 153, wherein the PLL is turned ON only in response to a valid FLP.

92 157. (New) The system of claim 153, wherein the microprocessor first examines a beginning portion of the FLP and, if that portion indicates that the transmission appears to be a valid FLP, turns on the PLL, and then reads the rest of the FLP to determine whether the FLP is valid.

158. (New) The system of claim 157, wherein the battery operated device turns OFF the PLL if the FLP is invalid.

159. (New) The system of claim 153, wherein the postamble reduces the amount of time the PLL is ON, thereby saving battery power when the battery operated device is not in the presence of a valid FLP.

160. (New) The system of claim 153, wherein the postamble increases the amount of time between the FLP and a RLP from the battery operated device, allowing the battery operated device enough time to obtain a PLL lock on an appropriate return link channel.

161. (New) The system of claim 142, wherein the battery operated device only responds to the transmission if it is a valid FLP.

162. (New) The system of claim 142, wherein the postamble shortens the amount of time that the receiver is ON.

163. (New) A system for conserving battery life in a battery operated device comprising:  
a receiver for receiving a transmission that includes a postamble; and  
a microprocessor coupled to the receiver, the microprocessor periodically partially awakening to determine whether the transmission is likely a FLP by turning ON the receiver only long enough to determine if the transmission includes a predetermined number of transitions.

164. (New) The system of claim 163, wherein the microprocessor fully awakens to determine whether the transmission is a valid FLP by examining at least a portion of the FLP data.

165. (New) The system of claim 163, wherein the postamble includes a stream of logical zeros.

166. (New) The system of claim 163, wherein the postamble includes a stream of logical ones.

167. (New) The system of claim 163, wherein the postamble begins with a stream of logical zeros and ends with a logical one.

168. (New) The system of claim 167, wherein the transition from logical zero to logical one signifies the end of the valid FLP.

169. (New) The system of claim 163, wherein the postamble begins with a stream of logical ones and ends with a logical zero.

170. (New) The system of claim 163, wherein the battery operated device includes a transmitter.

171. (New) The system of claim 170, wherein the postamble increases the amount of time between the FLP and a RLP from the battery operated device, allowing the battery operated device enough time to stabilize its transmitter on an appropriate return link channel.

172. (New) The system of claim 170, wherein the transmitter is turned ON only in response to a valid FLP.

173. (New) The system of claim 163, wherein the battery operated device includes a transmitter having a phase locked loop (PLL).

174. (New) The system of claim 173, wherein the postamble increases the amount of time that the battery operated device has to obtain a PLL lock on an appropriate return link channel.

175. (New) The system of claim 173, wherein the postamble shortens the amount of time that the PLL is ON.

176. (New) The system of claim 173, wherein the microprocessor first examines a beginning portion of the FLP and, if that portion indicates that the transmission appears to be a valid FLP, turns on the PLL, and then reads the rest of the FLP to determine whether the FLP is valid.

177. (New) The system of claim 176, wherein the battery operated device turns OFF the PLL if the FLP is invalid.

178. (New) The system of claim 173, wherein the PLL is turned ON only in response to a valid FLP.

179. (New) The system of claim 173, wherein the postamble reduces the amount of time the PLL is ON, thereby saving battery power when the battery operated device is not in the presence of a valid FLP.

180. (New) The system of claim 173, wherein the postamble increases the amount of time between the FLP and a RLP from the battery operated device, allowing the battery operated device enough time to obtain a PLL lock on an appropriate return link channel.

181. (New) The system of claim 163, wherein the postamble shortens the amount of time that the transmitter is ON.

182. (New) The system of claim 163, wherein the battery operated device only responds to the transmission if it is a valid FLP.

183. (New) The system of claim 163, wherein the postamble shortens the amount of time that the receiver is ON.

184. (New) The system of claim 163, wherein the FLP is Manchester encoded.

185. (New) A method for conserving battery life in a battery operated device comprising:  
receiving a wireless transmission;  
periodically partially awakening; and  
determining if the wireless transmission is likely a FLP by determining if the

transmission includes a valid postamble.

186. (New) The method of claim 185, wherein the postamble is valid if it includes a predetermined number of transitions.

187. (New) The method of claim 185, further including fully awakening to determine whether the transmission is a valid FLP by examining at least a portion of the FLP data.

188. (New) The method of claim 187, further including responding to the valid FLP via a transmitter.

189. (New) The method of claim 185, wherein a valid postamble increases the amount of time between the FLP and a RLP sent from the battery operated device, allowing the battery operated device enough time to stabilize its transmitter on an appropriate return link channel.

190. (New) The method of claim 185, wherein the battery operated device includes a transmitter having a phase locked loop (PLL).

191. (New) The method of claim 190, wherein a valid postamble increases the amount of time that the battery operated device has to obtain a PLL lock on an appropriate return link channel.

192. (New) The method of claim 190, wherein a valid postamble shortens the amount of time that the PLL is ON.

193. (New) The method of claim 190, wherein a valid postamble reduces the amount of time the PLL is ON, thereby saving battery power when the battery operated device is not in the presence of a valid FLP.

194. (New) The method of claim 190, wherein a valid postamble increases the amount of time between the FLP and a RLP from the battery operated device, allowing the battery operated device enough time to obtain a PLL lock on an appropriate return link channel.

A2 195. (New) A method for conserving battery life in a battery operated device comprising:  
targeting a specific device out of a plurality of devices;  
transmitting a transmission that includes a postamble;  
awakening to a first state to determine whether the transmission is likely a FLP by  
examining the postamble; and  
awakening to a second state if the transmission is likely a FLP.

196. (New) The method of claim 195, further including determining whether the FLP is valid.

197. (New) The method of claim 196, further including responding to the valid FLP.

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